



EXPEDITED PROCEDURE - EXAMINING GROUP 2124

S/N 09/637,532

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Dmitriy Rumynin et al.	Examiner:	David H. Malzahn
Serial No.:	09/637,532	Group Art Unit:	2124
Filed:	August 11, 2000	Docket No.:	1365.033US1
Title:	A PARALLEL COUNTER AND MULTIPLICATION LOGIC CIRCUIT		

#15/B  
1087  
2/25/04

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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FEB 13 2004

Technology Center 2100

In response to the Final Office Action mailed November 10, 2003, please amend the application as follows:

02/13/2004 DTESSEM1 00000113 190743 09637532  
02 FC:2201 258.00 DA

02/13/2004 DTESSEM1 00000087 190743 09637532  
02 FC:2201 258.00 DA

Void Date: 02/13/2004 DTESSEM1  
02/13/2004 DTESSEM1 00000087 190743 09637532  
02 FC:2201 258.00 CR